

ABSTRACT OF THE DISCLOSURE

An ATD circuit for row access, an ATD circuit for column access, and a mode determination circuit which determines which of the row access mode and column access mode is being executed are arranged in a pseudo SRAM. A mode determination signal is automatically generated in the chip to control the internal circuits. Upon detecting column address transition after the start of the cycle and activation of the sense amplifier, the mode determination circuit determines that column access starts. Upon detecting transition of a row address or a predetermined mode address after consecutive transition of the column address, the mode determination circuit determines that the column access mode is ended.